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**INTELLECTUAL PROPERTY LAW**  
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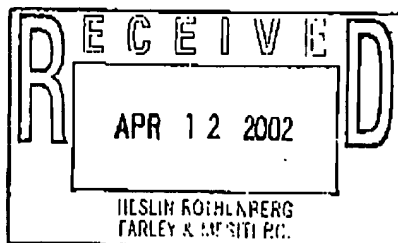
**\*\*\* PLEASE DELIVER IMMEDIATELY \*\*\***

**Date:** August 6, 2002  
**From:** Wayne F. Reinke (e-mail: [WFR@hrlmlaw.com](mailto:WFR@hrlmlaw.com))  
**No. of Pages:** 5 (Including this Page)  
**Fax No.:** (703) 872-9314  
**To:** Examiner Andrew L. Snizek  
Art Unit 2651  
U.S. Patent and Trademark Office  
Washington, D.C.  
**Re:** U.S. Patent Application Serial No.: 09/723,362  
Applicants: Chainer et al.  
Attorney Docket No.: YOR919940252US11  
Our File No.: 0559.055J

**Message:** Attached please find the following documents:

1. Response To Communication Regarding Omission In Reply To Prior Office Action (2 pages);
2. Copy of page 3 of the Response to Office Action filed on 03/27/2002 and received at the U.S. Patent and Trademark Office on 04/05/2002 (1 page); and
3. Copy of Acknowledgement Postcard (1 page).

WFR:rqs  
Attachments  
0229055J-1-A 802-Snizek



Applicants: Chainer et al.  
Serial No.: 09/723,362 Filing Date: 11/27/2000  
Title: METHOD AND APPARATUS FOR CORRECTING FOR SYSTEMATIC  
ERRORS IN TIMING PATTERN GENERATION  
\_\_\_\_ Patent App: Spec. \_\_\_\_ pps., \_\_\_\_ Clms. \_\_\_\_ pps., Abs. \_\_\_\_ pg.,  
\_\_\_\_ Drawings \_\_\_\_ sheets: \_\_\_\_ Formal, \_\_\_\_ Informal  
\_\_\_\_ Declaration \_\_\_\_ pps.: \_\_\_\_ Signed, \_\_\_\_ Unsigned  
\_\_\_\_ Assignment \_\_\_\_ pps.: \_\_\_\_ Assignment Recordation \_\_\_\_  
\_\_\_\_ IDS \_\_\_\_ pps.; \_\_\_\_ IIC \_\_\_\_ pps.; \_\_\_\_ Refs:  
\_\_\_\_ Statement of Relevance \_\_\_\_ pps.  
\_\_\_\_ Verified Statement Claiming Small Entity \_\_\_\_ pps.  
☒ Check \$ 110 (Extension Fee)  
☒ Transmittal Letter 1 pps.; ☒ duplicate  
☒ Amendment 6 pps.  
☒ Extension of Time 1 pps.; ☒ duplicate  
\_\_\_\_ Certificate of Mailing by "Express Mail" Label No. \_\_\_\_  
Mailed on March 27, 2002  
Docket No.: YOR919940252US11 File No.: 0559-055J



Atty/Sec

WPR/rqs

**Official**

-1-



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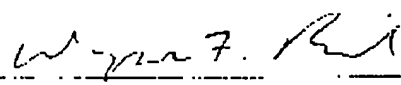
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Chainer et al. Confirmation No.: 6942  
Serial No.: 09/723,362 Group Art Unit: 2651  
Filed: 11/27/2000 Examiner: Sniezek, A.  
Title: METHOD AND APPARATUS FOR CORRECTING FOR SYSTEMATIC  
ERRORS IN TIMING PATTERN GENERATION

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being transmitted  
by facsimile transmission to: Examiner Andrew L. Sniezek,  
Group Art Unit 2651, United States Patent and Trademark  
Office, Washington, D.C. 20231, Facsimile No. 703-872-9324,  
on August 6, 2002.

  
Wayne F. Reinke  
Attorney for Applicants  
Registration No.: 36,650

Date of Signature: August 6, 2002.

To: Commissioner for Patents  
Washington, D.C. 20231

Mark Scholtz

**RESPONSE TO COMMUNICATION REGARDING OMISSION IN REPLY TO  
PRIOR OFFICE ACTION**

Dear Sir:

Applicants are in receipt of a communication mailed on July 12, 2002, response to which is due by August 12, 2002 without extension, indicating that the Response to Office Action (filed on March 27, 2002 and received at the U.S. Patent and Trademark Office on April 5, 2002) was not fully responsive because page 3 of the Response was missing.

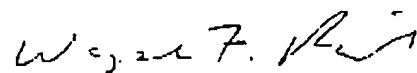
In response thereto, Applicants attach here a true and correct copy of page 3 of Applicants' Response to Office Action filed on March 27, 2002. Also attached is a copy of the postcard, indicating that all pages of the Response were received.

-2-

YOR919940252US11

It should be noted that, in response to a phone call from the Examiner on July 11, 2002, Applicants' attorney faxed a copy of page 3 directly to the Examiner.

If a telephone conference would be of assistance in advancing prosecution of the subject application, Applicants' undersigned attorney invites the Examiner to telephone him at the number provided.



Wayne F. Reinke  
Attorney for Applicants  
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Dated: August 6, 2002.

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-3-

YOR919940252US11

Therefore, Applicants submit that claim 25 cannot be anticipated by Yamakoshi et al. Claim 53 is an apparatus claim corresponding to method claim 25. Thus, the remarks made above with respect to claim 25 are equally applicable thereto.

Claim 60 recites a method for determining systematic time delays in the writing of trigger patterns on a storage medium of a storage device. The method comprises taking a plurality of measurements of at least one trigger pattern at a plurality of radial positions, and using the measurements to determine at least one systematic time delay.

Applicants submit that Yamakoshi et al. fails to disclose, for example, the taking step of claim 60. In fact, a computer search of Yamakoshi et al. reveals that the term "trigger pattern" is not even used. Thus, Applicants question how Yamakoshi et al. could possibly disclose taking a plurality of measurements of at least one trigger pattern at a plurality of radial positions, as claimed in claim 60. Moreover, Yamakoshi et al. also could not disclose using those measurements, let alone using them to determine at least one systematic time delay, as claimed in claim 60.

Therefore, Applicants submit that claim 60 cannot be anticipated by Yamakoshi et al. Claim 66 is an apparatus claim corresponding to method claim 60. Thus, the remarks made above with respect to claim 60 are equally applicable thereto.

Applicants submit that the dependent claims are allowable for the same reasons as the independent claims from which they directly or ultimately depend, as well as for their additional limitations.

For example, claim 26 recites that detecting the one or more circumferential systematic errors comprises computing an integral correction value for a time interval, wherein a non-zero integral indicates a circumferential error. Applicants submit there is nothing in Yamakoshi et al. regarding an integral correction value, and the Office Action